

Address phase

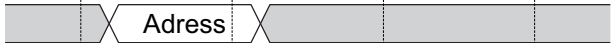
Read phase

Result phase

clk



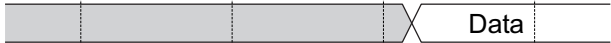
address



rd



rd_data



rdy_cnt

